

## **Application Data Sheet**

### **Application Information**

Application Type::	Regular
Subject Matter::	Utility
Suggested Group Art Unit::	N/A
CD-ROM or CD-R?::	None
Sequence submission?::	None
Computer Readable Form (CRF)?::	No
Title::	METHOD AND APPARATUS FOR ADAPTING MAC AND NETWORK PROCESSOR CORE TO PACKET FORMAT CHANGES AND FOR MAPPING RPR ARCHITECTURE OVER SPATIAL REUSE ARCHITECTURE
Attorney Docket Number::	04303/000N239-US0
Request for Early Publication?::	No
Request for Non-Publication?::	No
Total Drawing Sheets::	8
Small Entity?::	No
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

### **Applicant Information**

Applicant Authority Type::	Inventor
Primary Citizenship Country::	US
Status::	Full Capacity
Given Name::	Sanjay
Family Name::	Bhardwaj
City of Residence::	Fremont

State or Province of Residence:: CA  
Country of Residence:: US  
Street of mailing address:: 701 Bodega Court  
City of mailing address:: Fremont  
State or Province of mailing address:: CA  
Postal or Zip Code of mailing address:: 94539

### **Correspondence Information**

Correspondence Customer Number:: 38881

### **Representative Information**

Representative Customer Number:: 38881

### **Assignee Information**

Assignee name:: Infineon Technologies North America Corp.  
Street of mailing address:: Corporate Center  
Intellectual Property NA; 1730 North First  
Street  
City of mailing address:: San Jose  
State or Province of mailing address:: CA  
Postal or Zip Code of mailing address:: 95112